REMARKS:

This application has been reviewed in light of the Office Action mailed July 1, 2010. Reconsideration of this application in view of the below remarks is respectfully requested. By the present amendment, claim 1 is amended. No new subject matter is introduced into the disclosure by way of the present amendment. Claims 1-11 are pending in the application with claim 1 being in independent form.

Specification

The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. *See* 37 CFR 1.75(d)(1) and MPEP § 608.01 (0). Specifically, it is stated that the delineation of first, second, third and forth rows and columns does not have antecedent basis in the Specification. Paragraph [0047] is amended to provide antecedent basis for the recited phrases in the claims. The changes reflect what is shown in the Figures as originally filed and, therefore, do not add new matter. Therefore, applicant respectfully requests withdrawal of the objection. Paragraph [0037] is also amended to further clarify the recited terms of the claims.

Rejection under 35 U.S.C. §112, second paragraph

Claims 1-11 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. It is stated that it is unclear how a given row or column can be parallel to all four sides, as each row or column has portions that are perpendicular to the outer periphery. Applicant does not agree with this interpretation. The row and the column are arranged in parallel to some part of the sides and not to all four sides.

However, claim 1 is amended to clarify the meaning, reciting "parallel to at least a portion of...". Applicant respectfully requests withdrawal of the rejection.

Rejection under 35 U.S.C. §102(e)

Claims 1-2 and 4-11 are rejected under 35 U.S.C. §102(e) as being anticipated by U.S. 6,111,756 to Moresco. Claim 1 is amended to recite "the first group of I/O cells including a first row, a second row, a third row, a first column, a second column, and a third column, of interconnect pads disposed to encircle a center of the mounting member, and the second group of I/O cells including a fourth row, a fifth row, a sixth row, a fourth column, a fifth column, and a sixth column of interconnect pads disposed to encircle a center of the mounting member." Support for the amendment can be found at figures 5 and 6. The upper figure of figure 5 shows the arrangement of I/O cells, and lower figure of figure 5 shows the arrangement of interconnect pads in the I/O cell. Fig.6 shows the arrangement of interconnect pads of the first I/O cell and the second I/O cell.

Moresco teaches sets of signal traces for carrier 20 which run between the arrays of outside pads 24 and the array of inside pads 22. In both the inside and outside arrays 22 and 24, the signal pads (solid fill) are alternated in a "checkerboard" fashion. (See column 8, lines 46-53 and figure 8. Moresco also teaches Diamond shape, X shape, + shape and Windmill shape arrangements. (*See* Figure 14, for example). However, Moresco does not teach the first group of I/O cells including a first row, a second row, a third row, a first column, a second column, and a third column of electrode terminal arranged to surround a center of the semiconductor member, and the second group of I/O cells including a fourth row, a fifth row, a sixth row, a fourth column, a fifth column, and a sixth column of electrode terminals arranged to surround the center of the semiconductor member.

In addition, there is no teaching in Moresco of the first row, the second row, and the third row of the first group of I/O cells are parallel to one side of the semiconductor member, the first column, the second column, and the third column of the first group of I/O cells are parallel to an another side of the semiconductor member, the fourth row, the fifth row, and the sixth row of the second group of I/O cells are parallel to the one side of the semiconductor member, and the fourth column, the fifth column, and the sixth column of the second group of I/O cells are parallel to the another side of the semiconductor member. Therefore, claim 1 and its dependent claims are patentable over Moresco. Applicant respectfully requests withdrawal of the rejection.

Rejection under 35 U.S.C. §103(a)

Claim 3 is rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. 6,111,756 to Moresco and further in view of Applicant's Admitted Prior Art of Figure 1 (hereinafter "AAPA"). Figure 1 of AAPA does not teach the arrangement of I/O cells and electrode terminals recited in claim 1 and does not cure the deficiency of Moresco mentioned above. Claim 3 depends on claim 1 and therefore, is patentable for at least the same reason as claim 1.

Double patenting

Claims 1-11 are provisionally rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over claims 1-11 of copending Application No. 12/730,336. In response, Applicant submits a Terminal Disclaimer.

CONCLUSION

In view of the foregoing amendments and remarks, it is respectfully submitted that all claims presently pending in the application, namely, claims 1-11, are believed to be in condition for allowance and patentably distinguishable over the art of record.

If the Examiner should have any questions concerning this communication or feels that an interview would be helpful, the Examiner is requested to call Applicant's undersigned attorney at the number indicated below.

Respectfully submitted,

/Paul J. Esatto, Jr./

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